



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Jun Koyama
Serial No. : 10/800,035
Filed : March 15, 2004
Title : SEMICONDUCTOR DEVICE, CPU, IMAGE PROCESSING
CIRCUIT AND ELECTRONIC DEVICE, AND DRIVING METHOD
OF SEMICONDUCTOR DEVICE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

APPENDIX

- ☒ Verification of Translation (1 page)
- ☒ Substitute Specification (in English) (17 pages)
- ☒ Replacement Drawings (in English) (13 sheets)



Docket No.: 12732/218001

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In re Patent Application of:

Jun KOYAMA

Application No.: 10/800,035

Filed: March 15, 2004

For: SEMICONDUCTOR DEVICE,
CPU, IMAGE PROCESSING CIRCUIT AND
ELECTRONIC DEVICE, AND
DRIVING METHOD OF
SEMICONDUCTOR DEVICE

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)
) Examiner: Unknown
) Group Art Unit:
) Not Yet Assigned
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VERIFICATION OF TRANSLATION

Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

I, Ryosuke Murata, C/O Semiconductor Energy Laboratory Co., Ltd. 398, Hase, Atsugi-shi, Kanagawa-ken 243-0036 Japan, herewith declare:

that I am well acquainted with both the Japanese and English Languages; and

that to the best of my knowledge and belief the followings is a true and correct translation of the US Patent Application No. 10/800,035 filed on March 15, 2004.

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date: this 31th day of August, 2004

Name: Ryosuke Murata